

Agenda Sun Application Tuning Seminar

February 18-20, 2003

University of Mining and Metallurgy

Krakow

Poland

Tuesday February 18

Welcome and Introduction	09:00-09:15
Prologue	09:15-09:45
The Memory Hierarchy	09:45-10:30
<i>Coffee/tea break</i>	<i>10:30-10:45</i>
Compiler Options - Part I	10:45-11:30
Lab 1 - Getting started and Compiler Options	11:30-12:15
Review Lab 1	12:15-12:30
<i>Lunch break</i>	<i>12:30-13:30</i>
Compiler Options - Part II	13:30-14:00
Performance Analysis Tools	14:00-15:00
Demo - Performance Analyzer	15:00-15:30
<i>Coffee/tea break</i>	<i>15:30-15:45</i>
Architecture Overview	15:45-16:30
Serial Optimization Techniques	16:30-17:30

Wednesday February 19

Modulo Scheduling	09:00-10:00
<i>Coffee/tea break</i>	<i>10:00-10:15</i>
Labs 2 and 3 - Performance Analyzer and Compiler Messages Review Labs 2 and 3	10:15-11:00 11:00-11:15
Lab 4 - Manual Tuning Review Lab 4	11:15-12:15 12:15-12:30
<i>Lunch break</i>	<i>12:30-13:30</i>
Performance Tuning Case Studies	13:30-14:15
Introduction Into Parallelization	14:15-15:00
<i>Coffee/tea break</i>	<i>15:00-15:15</i>
Data Dependency Analysis Automatic Parallelization	15:15-15:45 15:45-16:30
Lab5 - Automatic Parallelization Review Lab 5	16:30-17:15 17:15-17:30

Thursday February 20

The Shared Memory Toolbox	09:00-09:30
Explicit Shared Memory Parallelization with OpenMP - Part I	09:30-10:30
<i>Coffee/tea break</i>	<i>10:30-10:45</i>
Lab 6 - Explicit Parallelization Review Lab 6	10:45-11:00 11:00-11:15
Explicit Shared Memory Parallelization with OpenMP - Part II OpenMP Case Studies	11:15-11:45 11:45-12:30

- Close Of Seminar -